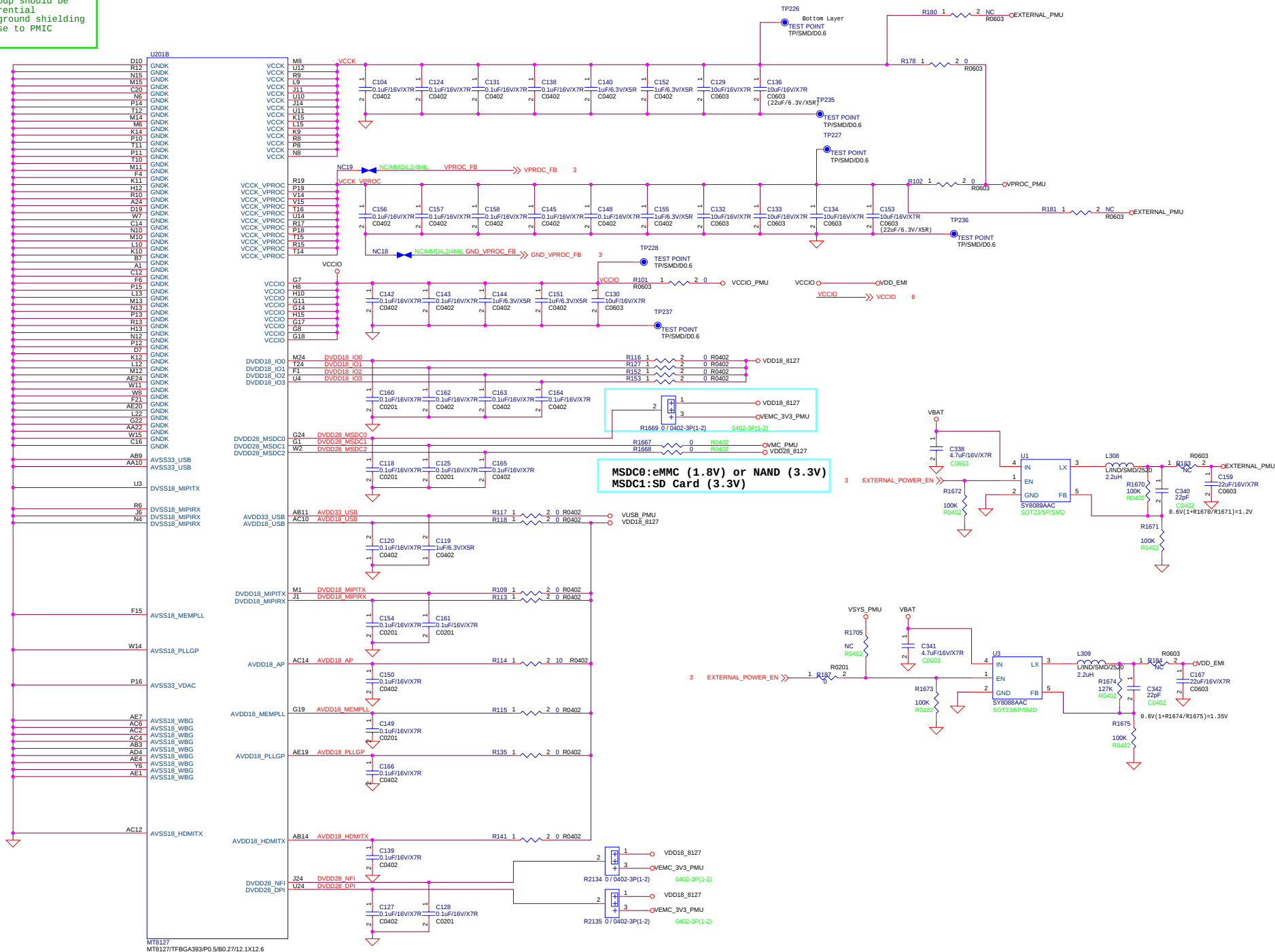
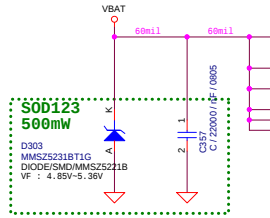
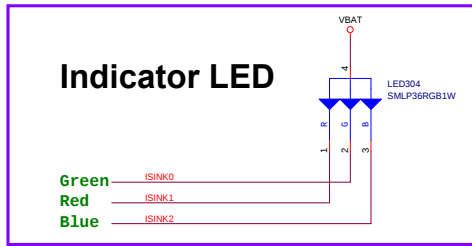
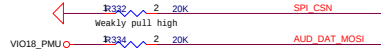


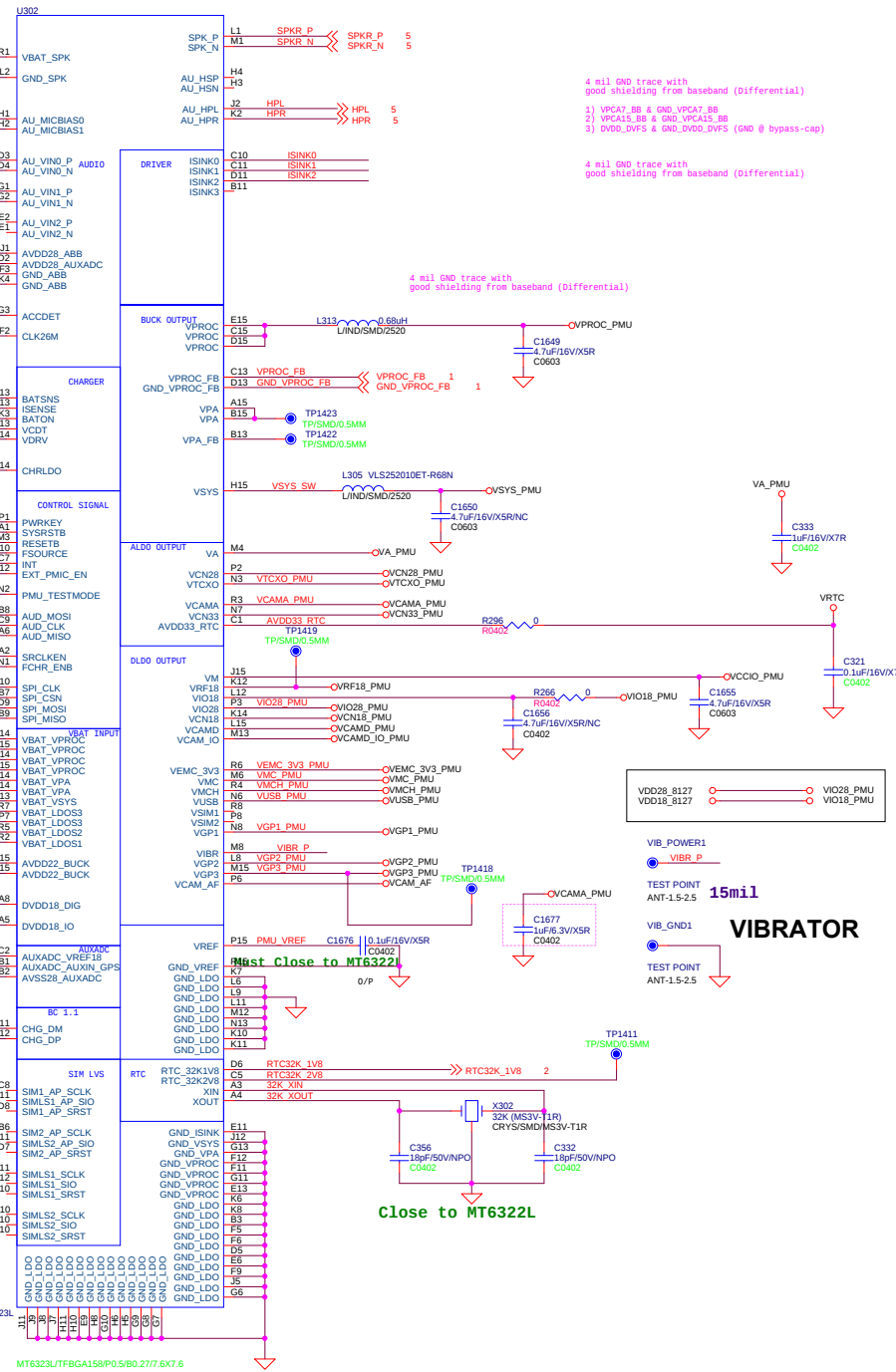
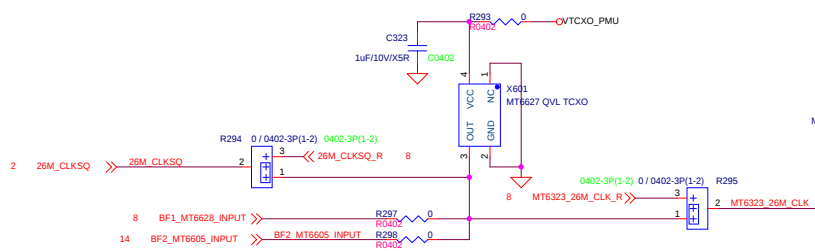
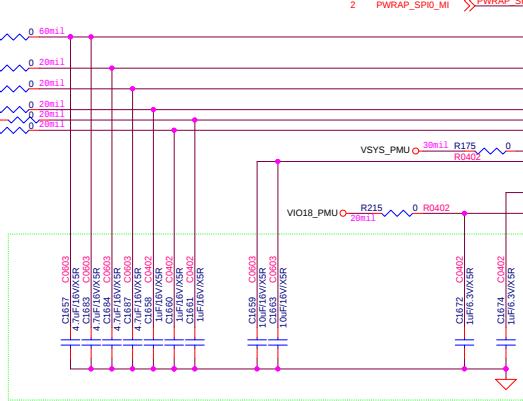
(1)VPROC_BB, GND pin of 1st cap group should be laid differential pair with ground shielding remote sense to PMIC



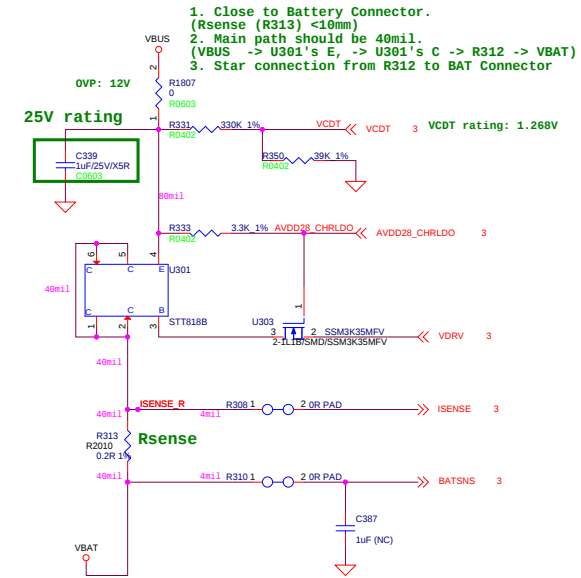
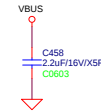
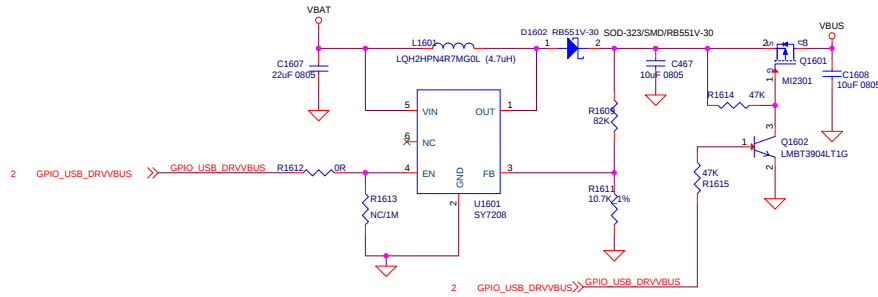
Symbol	LPDDR2/1.2V	PCDDR3L/1.35V	PCDDR3/1.5V	LPDDR1/1.8V	Default
SPI_CSN	H	L (20K)	H	L (20K)	PU
AUD_MOSI	L	H (20K)	H (20K)	L	PD



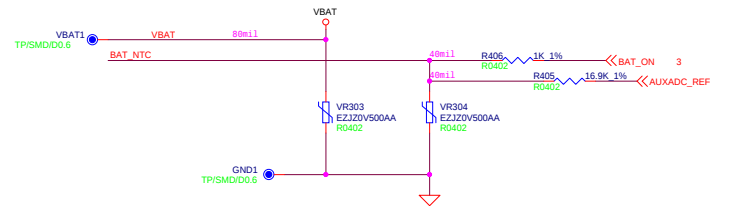
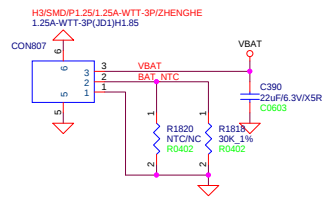
Add Zenar Diode
Place on the path
from VBAT to IC
(Battery connector
or test point or IO
connector)



OTG Power



BATTERY CONNECTOR

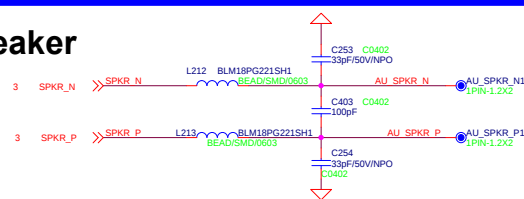


Thermal protection option 1: battery with NTC
(1) if battery NTC is 10kohm; R334=16.9K (+/-1%), R336=27K (+/-1%)
(2) if battery NTC is 47kohm; R334=61.9K (+/-1%), R336=100K (+/-1%)

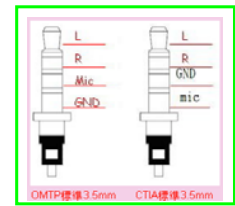
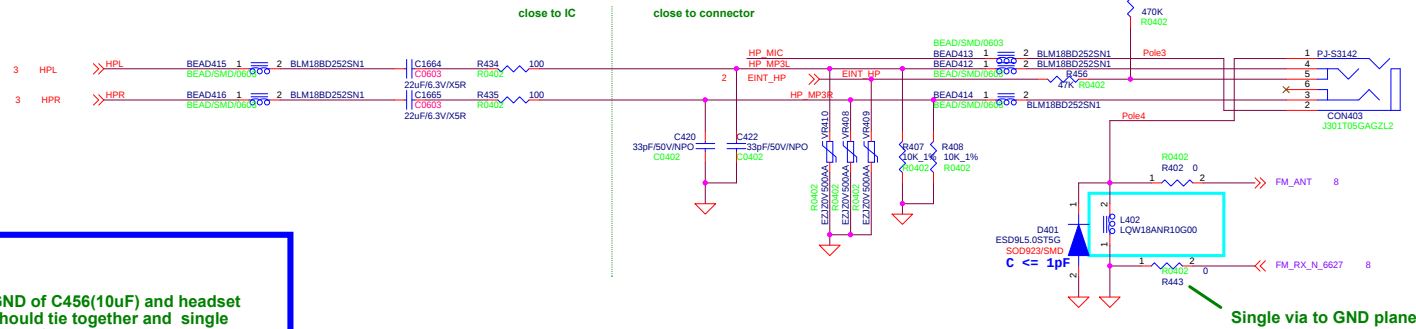
Thermal protection option 2: battery without NTC
(1) Delete R334, R336
(2) Use R331, NTC301 for thermal protection

綽 MT6323 AUXADC_VREF18 PIN,
and the path need shielding with GND

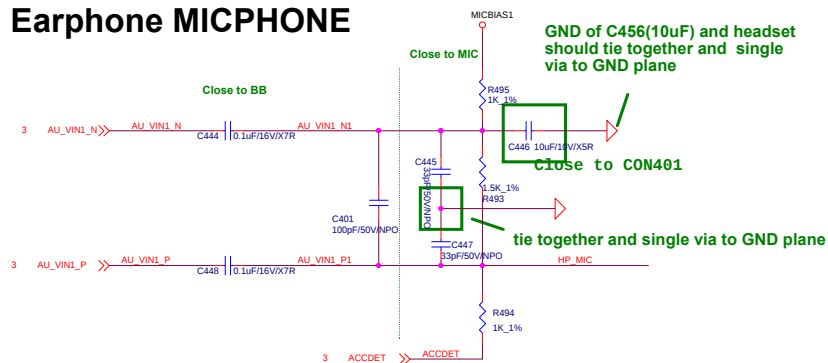
Speaker



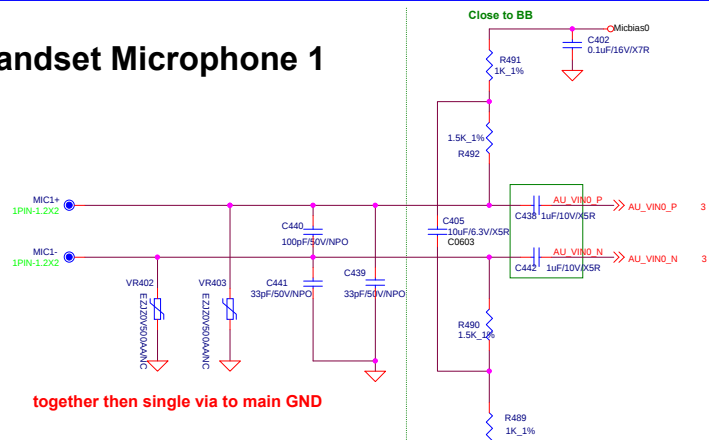
Earphone Audio

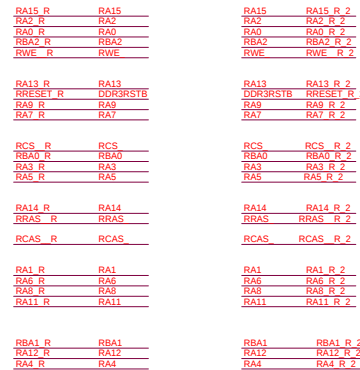
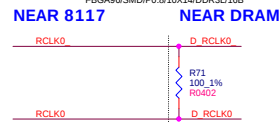
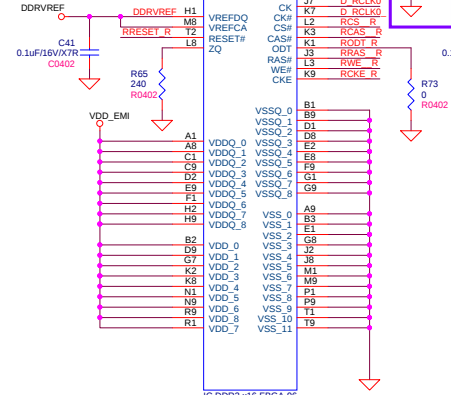
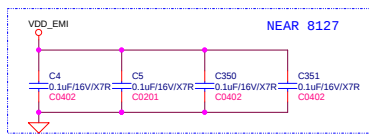
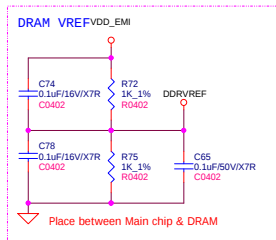
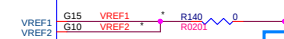
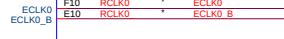
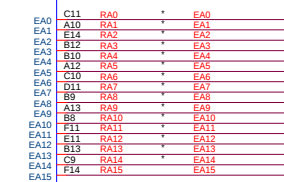
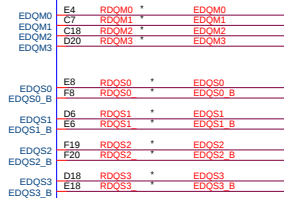
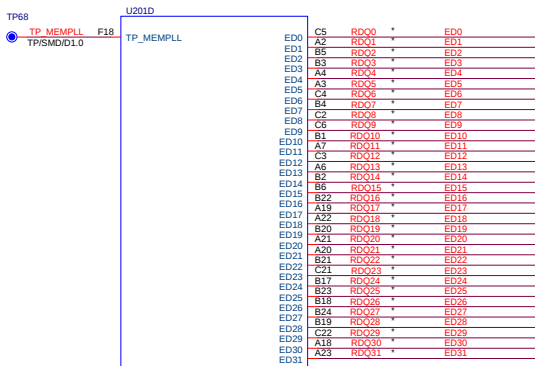


Earphone MICPHONE



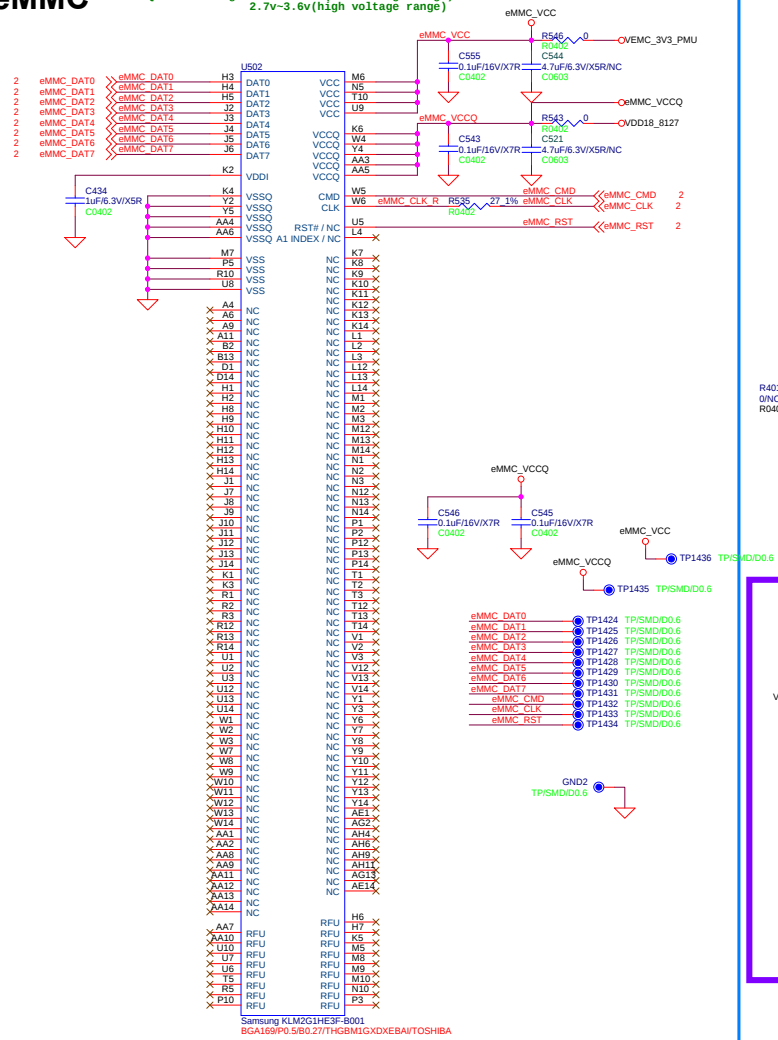
Handset Microphone 1



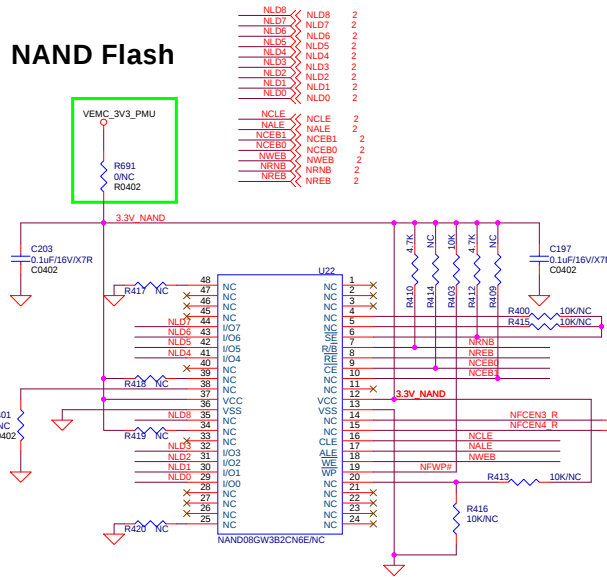


eMMC

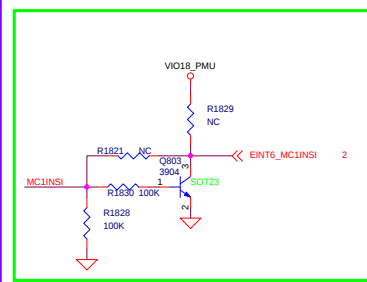
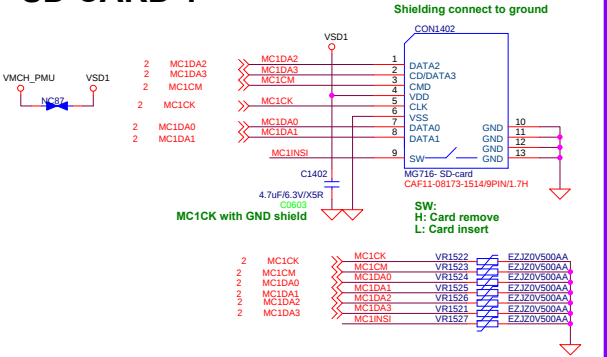
- ```
1. VCC : Core Voltage 2.7v ~ 3.6v
2. VCCQ : IO Voltage 1.7v~1.95v (low voltage range)
 2.7v~3.6v(high voltage range)
```



## NAND Flash

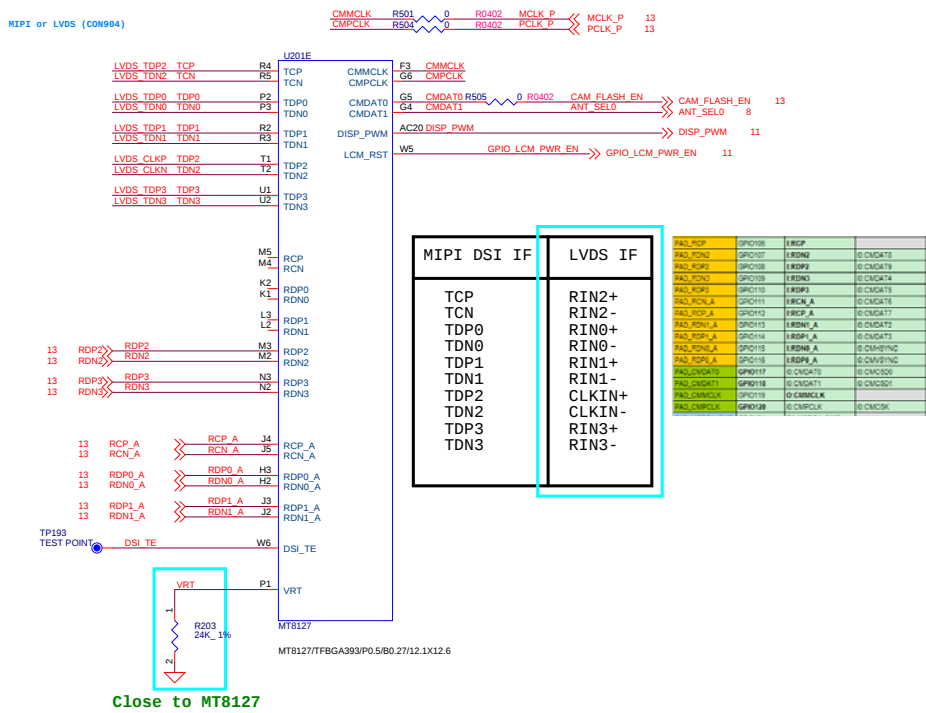


## SD CARD 1



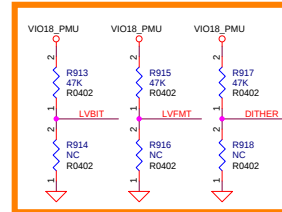
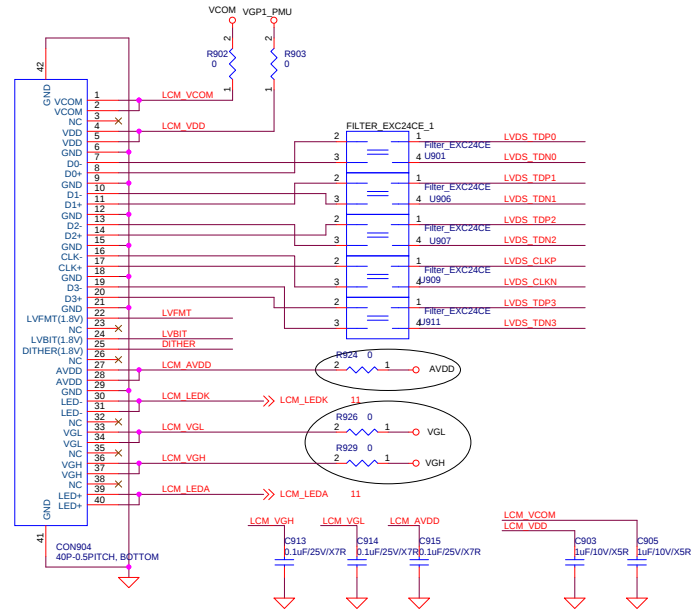
For SD card detect control:  
No card, MC1INSI\_R floating (PD), EINT9 --> High  
with card, MC1INSI\_R PU to VDD, EINT9 --> Low

MIPI or LVDS (CON904)

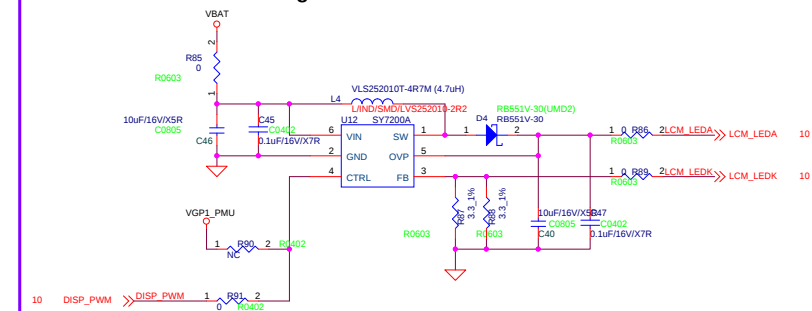


## MG916 LCD Connector (1280\*800 LVDS)

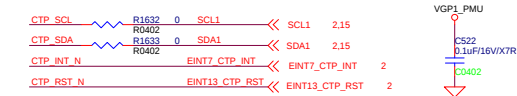
VCOM: 2.65V~3.65V  
VDD: 3.3V  
AVDD: 8.94V  
VGL: -8.18V  
VGH: 17.3V



## LED Backlight Driver

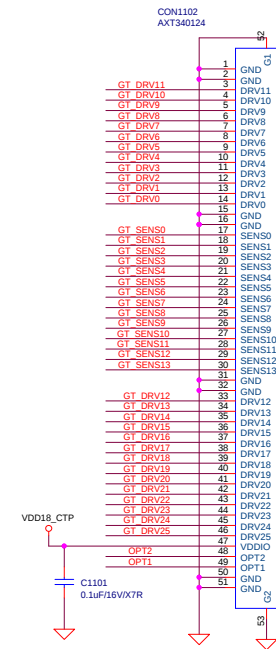
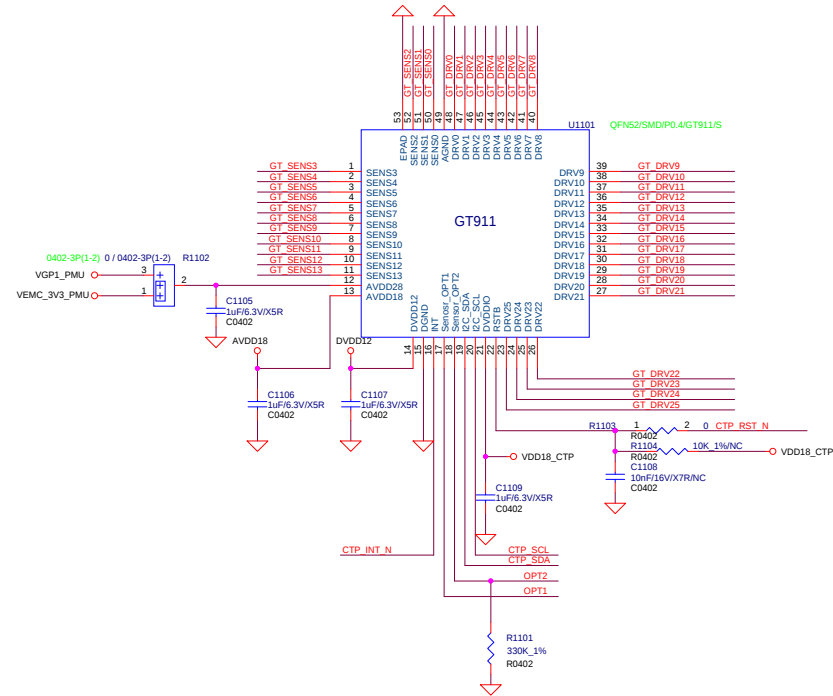
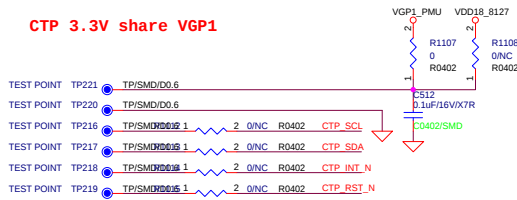


## CTP COB interface



### CTP DTB connector

CTP 3.3V share VGP1



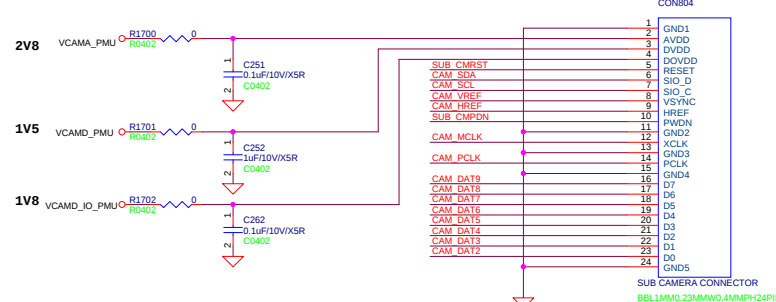
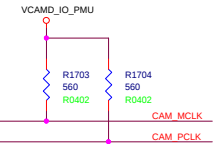
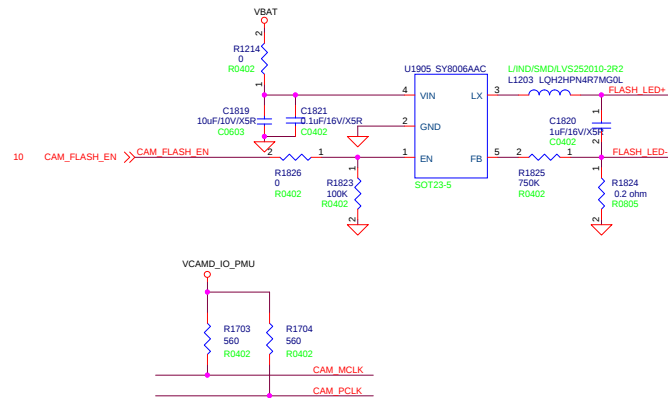
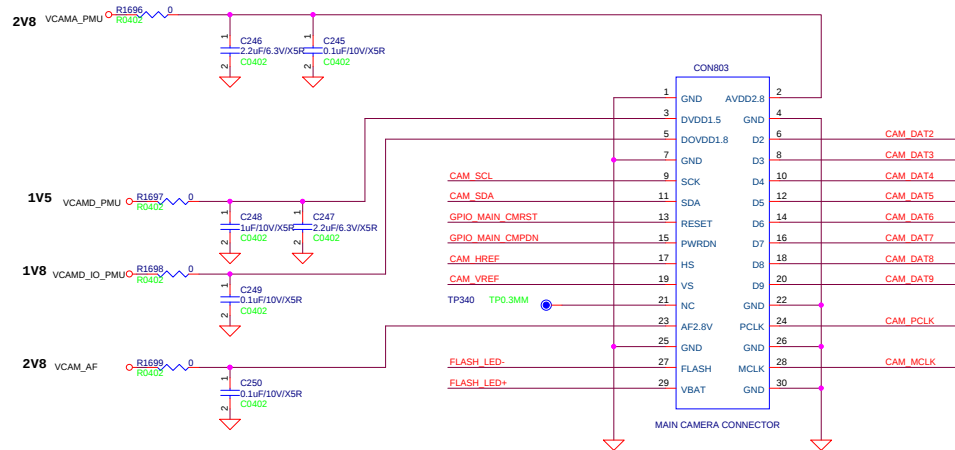
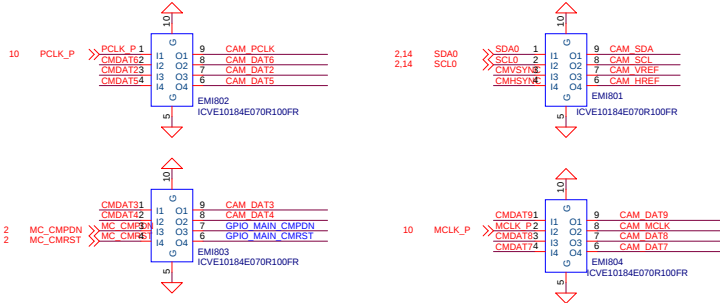
|            |         |          |         |
|------------|---------|----------|---------|
| RAD_PCP    | GPIO108 | IRPCP    |         |
| RAD_RDN2   | GPIO107 | IRDNG2   | ICMDAT8 |
| RAD_RDP2   | GPIO108 | IRDP2    | ICMDAT9 |
| RAD_RDN3   | GPIO109 | IRDND    | ICMDAT4 |
| RAD_RDP3   | GPIO110 | IRDP3    | ICMDAT5 |
| RAD_RDN4   | GPIO111 | IRDND_A  | ICMDAT6 |
| RAD_RDP4   | GPIO112 | IRDP4_A  | ICMDAT7 |
| RAD_RDN1_A | GPIO113 | IRDND1_A | ICMDAT2 |
| RAD_RDP1_A | GPIO114 | IRDP1_A  | ICMDAT3 |
| RAD_RDN1_A | GPIO115 | IRDND1_A | ICMDAT4 |
| RAD_RDP1_A | GPIO116 | IRDP1_A  | ICMDAT5 |
| RAD_RDN1_A | GPIO117 | IRDND1_A | ICMDAT6 |
| RAD_RDP1_A | GPIO118 | IRDP1_A  | ICMDAT7 |
| RAD_RDN1_A | GPIO119 | IRDND1_A | ICMDAT8 |
| RAD_RDP1_A | GPIO120 | IRDP1_A  | ICMDAT9 |

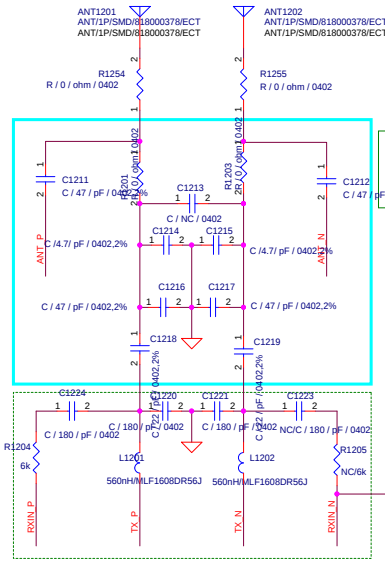
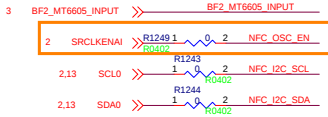
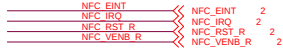
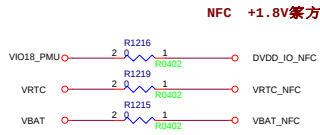
|    |       |        |         |
|----|-------|--------|---------|
| 10 | RDP2  | RDP2   | CMDAT9  |
| 10 | RDN2  | RDN2   | CMDAT8  |
| 10 | RDP3  | RDP3   | CMDAT5  |
| 10 | RDN3  | RDN3   | CMDAT4  |
| 10 | RCP_A | RCP_A  | CMDAT7  |
| 10 | RCN_A | RCN_A  | CMDAT6  |
| 10 | RDP0  | RDP0_A | CMVSYNC |
| 10 | RDN0  | RDN0_A | CMH5YNC |
| 10 | RDP1  | RDP1_A | CMDAT3  |
| 10 | RDN1  | RDN1_A | CMDAT2  |

## MG716 Parallel IF

|   |           |           |
|---|-----------|-----------|
| 2 | MC_CMRST  | MC_CMRST  |
| 2 | MC_CMPDN  | MC_CMPDN  |
| 2 | SUB_CMRST | SUB_CMRST |
| 2 | SUB_CMPDN | SUB_CMPDN |

## Camera

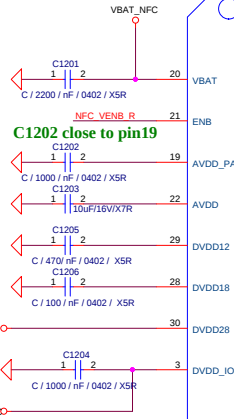




C1211, C1212, C1213, C1214, C1215, C1216, C1217, C1218, C1219 need to use 2% accuracy and 50V tolerance capacitor, PS: 0201 cap can't tolerance 50V

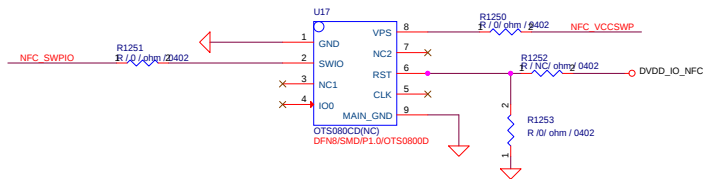
Components in this region use 5% accuracy

**ENB (NFC\_VEN)**  
1. Input pin  
2. Internal pull low  
3. Low active  
4. If default NFC would like to disable, please configure to high



C1202 close to pin19

**MT6605 QFN32 4x4**



T-Card

**POWER MODE[1:0]=[NFC\_RST:NFC\_VENB]**

| Power Mode                                                                   | NFC_RST | NFC_VENB |
|------------------------------------------------------------------------------|---------|----------|
| NFC enable (configure, R/W, card, polling loop, polling loop card listening) | 1       | 0        |
| NFC disable (HPD)                                                            | 0       | 1        |
| High battery card listening                                                  | 1       | 1        |
| Reset                                                                        | 0       | 0        |

C1210/R1202 are close to each other

**SYSRST\_B (NFC\_RST)**  
1. Input pin  
2. Internal pull high  
3. Low active

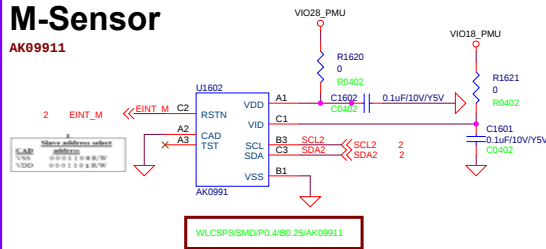
1. NFC\_OSC\_EN is output pin, and high active.  
2. Need to connect to host SRCLKENAI and SRCLKENAI pin need to be default low.

Only can use HW I2C. SW I2C is not allowed.

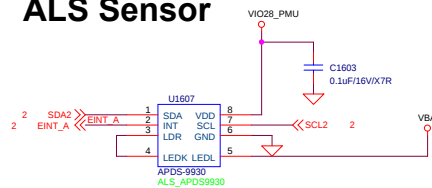
R1206 NC : XTAL MODE  
R1206 10K : Co-Clock

1. NFC\_IRQ is output pin, and high active.  
2. NFC\_IRQ is also strap pin and host I/F connected with IRQ need to be default low.

## AK09911



## ALS Sensor

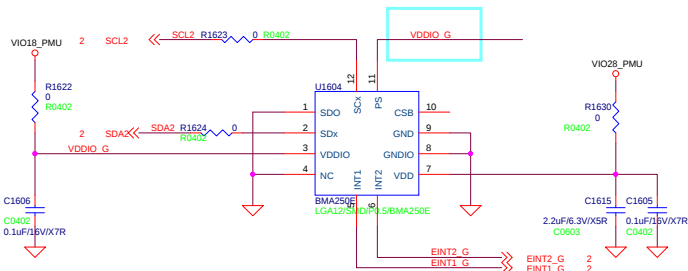


**I2C Address: 0x10 (Write:0x20, Read:0x21)**

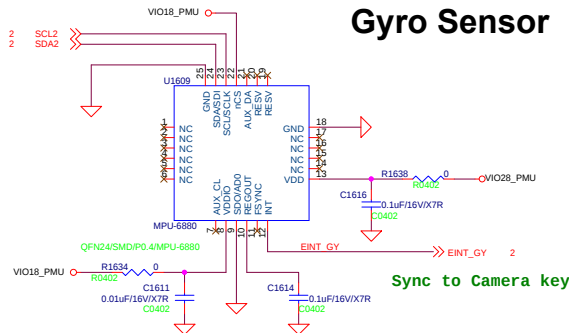
## BMA2501

## I2C mode

I2C Address: 0x4C (Write:0x98, Read:0x99)



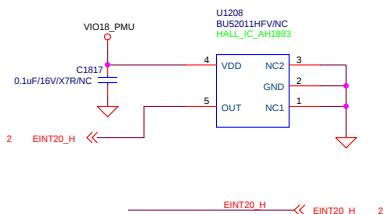
## Gyro Sensor



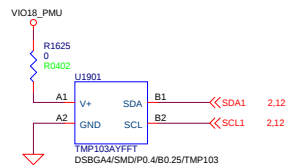
Sync to Camera key

**I2C Address: 0x68 (Write:0xD0, Read:0xD1)**

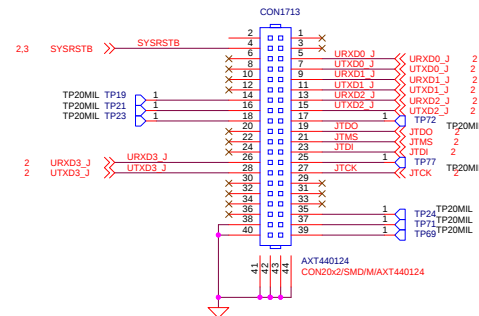
## Hall-Sensor



## Thermal Sensor A



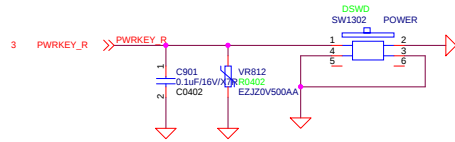
## Common Debug



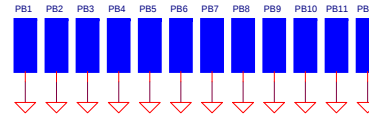


## Power Key

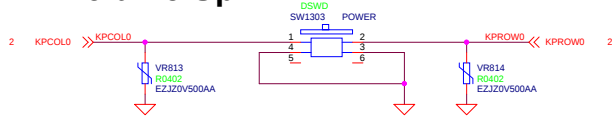
DO NOT put pull-up resistor on PWRKEY



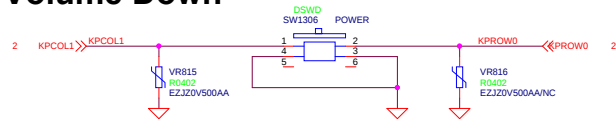
## Shielding



## Volume Up



## Volume Down



## USB HS IF

